

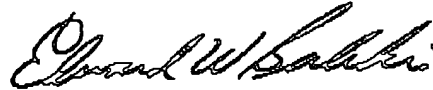
the timing that a signal transmitted by a memory device was received by a memory controller or a signal transmitted by a memory controller was received by a memory device. Nor do the cited references, taken either alone or in combination, disclose a memory device or memory controller performing that function.

After discussing the claimed invention and the cited references, the Examiner agreed that all of the claims patentably distinguish over the cited references.

All of the claims remaining in the application are now clearly allowable. Favorable consideration and a Notice of Allowance are earnestly solicited.

Respectfully submitted,

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EWB:dms

Enclosures:

Fee Transmittal Sheet (+copy)

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